



LAP-16128U / 16 channels personal computer based logic Analyzer



Minimum System Requirement			
OS supported	CPU	memory Size	Disk space
Microsoft Windows 98SE	166MHz or upgrade	128MB or more	100MB available disk space at least
Microsoft Windows Me	166MHz or upgrade	128MB or more	
Microsoft Windows 2000 Pro	300MHz or upgrade	128MB or more	
Microsoft Windows XP	300MHz or upgrade	256MB or more	

- A specification far in excess of its price
- 16 channels (LAP-16128U)
- 256Kbits memory depth/channel
- Compression ratio up to 255:1 depending on activity
- 100 MHz synchronous operation on all channels
- 200 MHz asynchronous operation (16 channels)
- 5ns glitch capture capability
- Powered from USB interface – ideal for laptops
- Easy to use Windows application
- Captured data can be printed or written to a TXT file or a CSV file for processing
- Timing/ State/ Single-processor/ Bus/ Real-time instruction trace analysis
- Flexible signal trigger options (rising edge, falling edge, either edge, high and low)
- Binary/ Decimal/ Hex display formats provided
- Selectable trigger position between 100% pre-trigger and 100% post trigger plus up to 16776191 clocks of post-trigger delay.
- Parts of the data are to be defaulted to don't care for the purposes of a search or a comparison
- Software updates downloadable from our web site

Specifications	
Channels	16 channels
Sampling Frequency	100Hz ~ 200MHz
Samples	256Kbits per channel
External Clock	Supported
Personal computer interface	USB 2.0 interface

Main set & Accessories	
Mainframe (130mm*100mm*30mm / 175g)	1 set
USB cable	1 piece
16 channel testing cable package (25cm) 8 pin *2 / 2 pins*1 / 1 pin*1	1 pack
Probe (testing hooks) 20 pieces/package	1 pack
Install Disk	1 piece
User Manual	1 book
Quality Guarantee Certificate	1 piece

